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Bib Data Sheet

CONFIRMATION NO. 5851

SERIAL NUMBER 10/015,921	FILING DATE 12/10/2001  RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. RPS920010132US1
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None PD

\*\* CONTINUING DATA \*\*\*\*\*

None PD

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 01/11/2002

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	NC	2	12	3
Verified and Acknowledged	Paul Jinkins PI Examiner's Signature Initials				

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## TITLE

Method and system for use of a field programmable gate array (FPGA) cell for controlling access to on-chip functions of a system on a chip (SOC) integrated circuit

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees  <input type="checkbox"/> 1.16 Fees ( Filing )  <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )
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